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12/29/00
jc15 U.S. PTO

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Washington D.C. 20231
Box Patent Applications

Case Docket No. NECW 18.159
Filed by Express Mail
(Receipt No. EL522398305US)
on December 29, 2000
pursuant to 37 CFR 1.10
by Brendy Lynn Belony

A
jc962 U.S. PTO
09/751979
12/29/00

S I R:

Transmitted herewith for filing is: ☒ a new application
[] a c-i-p application of S.N. _____ filed _____

Inventor(s): Atsushi NISHIZAWA

For: **MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING
SIMULTANEOUS FORMATION OF VIA HOLE REACHING METAL WIRING AND CONCAVE
GROOVE IN INTERLAYER FILM AND SEMICONDUCTOR INTEGRATED CIRCUIT
MANUFACTURED WITH THE MANUFACTURING METHOD**

Enclosed are:

- ☒ 6 sheets of drawings.(Figs. 1A-E, 2A-D, 3A-C, 4A-C, 5-6, 7A-C)
- ☒ Specification, including claims and abstract (23 pages)
- ☒ Declaration
- ☒ An assignment of the Invention to NEC CORP
- ☒ A certified copy of Japanese Application No(s).2000-009221
- ☒ An associate power of attorney
- [] A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27
- ☒ Post card
- ☒ Recording fee (as indicated below)
- [] Information Disclosure Statement, PTO-1449, copies of _____ references
- [] Other _____
- [] Other _____

	Col. 1	Col. 2
FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	12-20 =	0
INDEP CLAIMS	2-3 =	0
[] MULTIPLE DEPENDENT CLAIMS PRESENTED		

*If the difference in Col. 1 is less than
zero, enter "0" in Col. 2

SMALL ENTITY	
RATE	FEE
	\$355
x 9 =	\$
X 40 =	\$
x 135 =	\$
TOTAL	\$

OTHER THAN A SMALL ENTITY	
RATE	FEE
	\$710
x 18 =	\$
x 80 =	\$
x 270 =	\$
TOTAL	\$710

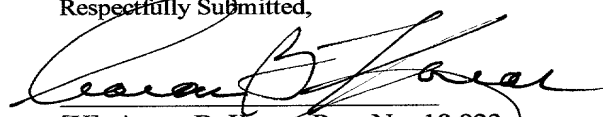
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Helfgott & Karas, P.C.
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Date: 12/29/00

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Respectfully Submitted,



- ☒ Aaron B. Karas, Reg. No. 18,923
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